

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Jourdan et al.) Examiner: N/A
Application No.: 10/676,310) Art Unit: 2154
Filed: September 30, 2003)
For: METHOD AND SYSTEM FOR MULTIPLE)
BRANCH PATHS IN A MICROPROCESSOR)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR STATUS OF APPLICATION

Dear Commissioner:

The last correspondence received from the U.S. Patent and Trademark Office (PTO) was a "Notice of Publication mailed March 31, 2005". Please inform us on the current status of the above-referenced patent application.

If there are any further charges, please charge them to Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 8/11/05

Lisa Tom
Lisa Tom
Reg. No. 52,291

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1026
(206) 292-8600

FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

August 11, 2005

Date of Deposit

Adrian Villarreal

Name of Person Mailing Correspondence

[Signature]
Signature

August 11, 2005
Date